

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	95	@ay<="2000" and ((content adj address\$4 adj memory) or CAM or TCAM or ((embedd\$3 or integrat\$3 or resident or coupl\$3) near3 memory)) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:14
L2	93	@ad< "20000919" and ((content adj address\$4 adj memory) or CAM or TCAM or ((embedd\$3 or integrat\$3 or resident or coupl\$3) near3 memory)) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:16
L3	3	@ad< "20000919" and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:17
L4	1	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:21
L5	0	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:21

L6	0	709/215.ccls. and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:22
L7	0	709/224.ccls. and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:23
L8	0	709/238.ccls. and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:23
L9	0	709/245.ccls. and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:23
L10	4	"709"/\$ and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:23
L11	1	"710"/\$ and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:24
L12	3	"711"/\$ and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:26
L13	1	"714"/\$ and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:27

L14	1	"718"/\$ and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:27
L15	0	"455"/\$ and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:28
L16	9	"370"/\$ and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:28
L17	0	"340"/\$ and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU\$3 or (arithmetic adj logic\$2 adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/01/16 18:30
S1	65	@ad<"20010824" and (ternary adj content adj addressable adj memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:03
S2	7	@ad<"20010824" and (ternary adj content adj addressable adj memory) and (CPU or ALU) and (match\$3) and address and network and IP	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:33
S3	6	@ad<"20010824" and (ternary adj content adj addressable adj memory) and (CPU or ALU) and (match\$3) and address and network and IP and classif\$7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:08
S4	0	@ad<"20010824" and (ternary adj content adj addressable adj memory) and (CPU or ALU) and (match\$3) and address and IP and classif\$7 and (stuff or unstuff)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:09

S5	0	@ad<"20010824" and (ternary adj content adj addressable adj memory) same (CPU or ALU) and (lookup near address near IP) and ((classif\$7 or process\$3 or match\$3) near packet)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:10
S6	0	@ad<"20010824" and (ternary adj content adj addressable adj memory) same (CPU or ALU) and (lookup same address same IP) and ((classif\$7 or process\$3 or match\$3) same packet)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:11
S7	4	@ad<"20010824" and (ternary adj content adj addressable adj memory) and (CPU or ALU) and (lookup same address same IP) and ((classif\$7 or process\$3 or match\$3) same packet)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:14
S8	11	@ad<"20010824" and (ternary adj content adj addressable adj memory) and (CPU or ALU or TCAM) and (lookup same address) and ((classif\$7 or process\$3 or match\$3) same packet)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:19
S9	0	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) and (CPU or (central adj processing adj unit)) and (ALU or (arithmetic adj logic adj unit)) and (lookup same address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:20
S10	0	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) and (CPU or (central adj processing adj unit)) and (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:20

S11	21	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) and (CPU or (central adj processing adj unit) or ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 20:46
S12	2	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) same (CPU or (central adj processing adj unit) or ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/17 19:21
S18	0	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) with (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 20:47
S19	0	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 18:09
S20	51	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 20:49
S21	21	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 20:57
S22	51	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/20 20:50

S23	4	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or (arithmetic adj logic adj unit)) same (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 19:20
S24	35	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) same (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 19:21
S25	5	@ad<"20010824" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) with (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 19:21
S26	5	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) with (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 19:26
S27	30	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) same (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:13
S28	43	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) adj unit)) same (within or inside)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:42
S29	30	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) same (within or inside or embed\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:47

S30	5	@ad< "20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) with (within or inside or embed\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:43
S31	8	@ad< "20000919" and ((content adj addressable adj memory) or TCAM or CAM) near (ALU or ((arithmetic or logic) adj unit)) and (within or inside or embed\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:50
S32	0	@ad< "20000919" and ((content adj addressable adj memory) or TCAM or CAM) near (ALU or ((arithmetic or logic) adj unit)) same (within or inside or embed\$4 or in or compris\$3 or contain \$3) same (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:51
S33	1	@ad< "20000919" and ((content adj addressable adj memory) or TCAM or CAM) near (ALU or ((arithmetic or logic) adj unit)) and (within or inside or embed\$4 or in or compris\$3 or contain \$3) same (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 16:45
S34	23	@ad< "20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) and (within or inside or embed\$4 or in or compris\$3 or contain \$3) same (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:03

S35	1	@ad<"20000919" and ((content adj addressable adj memory) or TCAM or CAM) with (ALU or ((arithmetic or logic) adj unit)) same (within or inside or embed\$4 or in or compris\$3 or contain \$3) same (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 20:55
S36	13	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) near unit)) same (within or inside or embed\$4 or in or compris\$3 or contain \$3) same (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:12
S37	22	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) near unit)) same (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:12
S38	22	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) near unit)) same (CPU or (process\$3 near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:12
S39	62	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) near unit)) and (within or inside or embed\$4 or in or compris\$3 or contain \$3) same (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:12

S40	23	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM) same (ALU or ((arithmetic or logic) near unit)) and (within or inside or embed\$4 or in or compris\$3 or contain \$3) near (CPU or (process \$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:14
S41	2875	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM or ALU or ((arithmetic or logic) near unit)) and (within or inside or embed\$4 or in or compris\$3 or contain \$3) near (CPU or (process \$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:14
S42	283	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM or ALU or ((arithmetic or logic) near unit)) with (within or inside or embed\$4 or in or compris\$3 or contain \$3) near (CPU or (process \$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:15
S43	37	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM or ALU or ((arithmetic or logic) near unit)) near (within or inside or embed\$4 or in or compris\$3 or contain \$3) near (CPU or (process \$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:17
S44	35	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM or ALU or ((arithmetic adj logic) adj unit)) near (within or inside or embed\$4 or in or compris\$3 or contain \$3) near (CPU or (process \$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:20

S45	0	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM) near (within or inside or embed\$4 or in or compris\$3 or contain \$3) near (CPU or (process \$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:21
S46	27	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM) with (within or inside or embed\$4 or in or compris\$3 or contain \$3) near (CPU or (process \$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:22
S47	209	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM or CAM) with (within or inside or embed\$4 or in or compris\$3 or contain \$3) with (CPU or (process \$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:22
S48	13	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM) with (within or inside or embed\$4 or in or compris\$3 or contain\$3) with (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:27
S49	0	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM) with (within or inside or embed\$4 or in or compris\$3 or contain \$3) with (CPU or (process \$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:27
S50	1	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM) same (within or inside or embed\$4 or in or compris\$3 or contain \$3) with (CPU or (process \$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:27

S51	1	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM) same (within or inside or embed\$4 or in or compris\$3 or contain \$3) with (CPU or (process \$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:35
S52	0	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM) near (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:36
S53	1	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM) with (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:36
S54	1	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM) same (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:36
S55	17	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM) and (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:45
S56	21	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM) and (CPU or ((process\$3 or control) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:48
S57	326	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM) same (CPU or ((process \$3 or control) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:49
S58	7	@ad<"20000919" and ((content adj address\$4 adj memory) or TCAM) near (CPU or ((process\$3 or control) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/21 21:49
S59	35	@ad<"20000919" and (ALU or ((arithmetic adj logic) adj unit)) near (within or inside or embed \$4 or in or compris\$3 or contain\$3) near (CPU or (process\$3 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 16:46

S60	0	@ad<"20000919" and (ALU or ((arithmetic adj logic) adj unit)) near (within or inside or embed \$4 or in or compris\$3 or contain\$3) near (CPU or (process\$3 adj unit)) near memory	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 16:47
S61	18	@ad<"20000919" and (ALU or ((arithmetic adj logic) adj unit)) near (within or inside or embed \$4 or in or compris\$3 or contain\$3) near (CPU or (process\$3 adj unit)) same (TCAM or CAM or memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 16:47
S65	0	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM or (ternary adj CAM)) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 18:10
S66	2	@ad<"20010824" and ((ternary adj content adj addressable adj memory) or TCAM or (ternary adj CAM)) with (CPU or (process\$3 adj unit) or ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 18:11
S67	2	@ad<"20000919" and ((ternary adj content adj addressable adj memory) or TCAM or (ternary adj CAM)) same (CPU or (process\$3 adj unit) or ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 18:21
S68	41	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or (process\$3) or ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 19:34

S69	27	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) with (CPU or (process\$3) or ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 18:23
S71	12	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) near (within or compris\$3 or contain\$3 or in or embed \$4) near (CPU or (process \$3) or ALU or ((arithmetic or logic)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/22 19:38
S72	21	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) near (within or compris\$3 or contain\$3 or in or embed \$4 or includ\$3) near (CPU or (process\$3) or ALU or ((arithmetic or logic)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/23 14:51
S86	13	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) near (within or compris\$3 or contain\$3 or in or embed \$4 or includ\$3) near (ALU or ((arithmetic or logic)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/23 14:55
S87	96	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with (within or compris\$3 or contain\$3 or in or embed \$4 or includ\$3) near (ALU or ((arithmetic or logic)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/23 14:56

S88	1	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ((locat \$3 near within) or compris \$3 or contain\$3 or in or embed\$4 or includ\$3) near (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:22
S88	10	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ((locat \$3 near within) or compris \$3 or contain\$3 or in or embed\$4 or includ\$3) near (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:20
S89	0	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ((locat \$3 near within) or compris \$3 or contain\$3 or in or embed\$4 or includ\$3 or coupl\$3) near (ALU or ((arithmetic or logic) adj unit)) same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:16
S100	3	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ((locat \$3 near within) or compris \$3 or contain\$3 or in or embed\$4 or includ\$3 or coupl\$3) near (ALU or ((arithmetic or logic) adj unit)) and CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:16
S101	0	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ((locat \$3 near within) or compris \$3 or contain\$3 or in or embed\$4 or includ\$3 or coupl\$3) near (ALU or ((arithmetic or logic) adj unit)) same (CPU or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:16

S102	0	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) with ((locat\$3 near within) or compris\$3 or contain\$3 or in or embed\$4 or includ\$3) with (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:20
S103	0	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) with (locat\$3 or compris\$3 or contain\$3 or in or embed\$4 or includ\$3 or coupl\$3) with (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:21
S104	0	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (locat\$3 or compris\$3 or contain\$3 or in or embed\$4 or includ\$3 or coupl\$3) with (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:21
S105	2	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (locat\$3 or compris\$3 or contain\$3 or in or embed\$4 or includ\$3 or coupl\$3) same (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:22
S106	0	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic or logic) adj unit)) same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:23
S107	3	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic or logic or processing) adj unit)) same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:31

S108	11	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or ALU or ((arithmetic or logic or processing) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:27
S109	44	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) and (ALU or ((arithmetic or logic or processing) adj unit)) same CPU	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/15 20:31
S110	1	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with ("near" or within or compris\$3 or contain\$3 or in or embed\$4 or includ\$3) near (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:23
S111	2	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with (inside or within or connect\$3 or compris\$3 or contain\$3 or in or embed\$4 or includ\$3) near (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:23
S112	28	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with (inside or within or connect\$3 or compris\$3 or contain\$3 or in or embed\$4 or includ\$3) with (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:31
S113	0	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) with (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:32

S114	0	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:32
S115	2	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or ALU or ((arithmetic or logic) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 17:50
S116	16	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or ALU or ((arithmetic or logic) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:07
S117	16	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or ALU or ((arithmetic or logic or processing) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:30
S118	124	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or ALU or ((arithmetic or logic or processing) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:07
S119	80	((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) with (CPU or ALU or ((arithmetic or logic or processing) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:07

S121	4831	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) same (CPU or ALU or ((arithmetic or logic or processing) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:30
S122	2455	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with (CPU or ALU or ((arithmetic or logic or processing) adj unit) or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 18:31
S123	61	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM) or CAM) with (ALU or ((arithmetic or logic or processing) adj unit)) with (CPU or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:35
S128	0	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) with (ALU or ((arithmetic or logic or processing) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:36
S129	2	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic or logic or processing) adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:37
S130	0	@ad< "20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic or logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/25 19:37

S131	6	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) and (ALU or ((arithmetic or logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 14:28
S134	191	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic or logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 14:28
S135	3	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) same (CPU or ALU or ((arithmetic or logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 14:31
S136	29	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) and (CPU or ALU or ((arithmetic or logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 15:18
S138	17	(Pandya or whisenhunt or Freescale) and ALU and CAM	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 15:14
S140	1	Mihir near pandya and ALU and CAM	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 15:15
S141	9	Mihir near pandya	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 15:15
S142	628	@ad<"20000919" and ((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) same (resident or within or coupl\$3 or locat\$3 or connect\$3) same (CPU or ALU or ((arithmetic or logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 15:19

S143	289	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) with (resident or within or in or coupl\$3 or locat\$3 or connect\$3) with (CPU or ALU or ((arithmetic or logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 15:20
S144	92	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) near5 (resident or within or in or coupl\$3 or locat\$3 or connect\$3 or contain\$3) with (CPU or ALU or ((arithmetic or logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 15:57
S145	10	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) near5 (resident or within or in or coupl\$3 or locat\$3 or connect\$3 or contain\$3) with (ALU or ((arithmetic or logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 16:01
S146	20	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) with (ALU or ((arithmetic adj logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 16:25
S147	42	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) same (ALU or ((arithmetic adj logic) near unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 16:25
S148	35	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) with (ALU or (arithmetic near (logic or unit)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 16:36

S149	75	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) same (ALU or (arithmetic near (logic or unit))) same (CPU or (central adj processing or unit) or microprocessor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 16:37
S150	27	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) same (ALU or (arithmetic near (logic or unit))) same (contain\$3 or compris\$3 or within or in or includ \$3) with (CPU or (central adj processing or unit) or microprocessor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 16:37
S151	27	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) same (ALU or (arithmetic near (logic or unit))) same (contain\$3 or compris\$3 or within or in or includ\$3 or inside) with (CPU or (central adj processing or unit) or microprocessor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 16:38
S152	32	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) same (ALU or (arithmetic near (logic or unit))) same (contain\$3 or compris\$3 or within or in or includ\$3 or inside or coupl\$3 or connect\$3) with (CPU or (central adj processing or unit) or microprocessor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 16:47

S153	74	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or CAM or TCAM or (ternary adj CAM)) same (ALU or (arithmetic near (logic or unit))) with (CPU or (central adj processing or unit) or microprocessor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 17:03
S154	76	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) near CAM and (address or packet) near (lookup or match\$3 or process\$3 or stuff\$3 or unstuff\$3 or classif\$7)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 17:05
S155	19	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) near CAM same (address or packet) near (lookup or match\$3 or process\$3 or stuff\$3 or unstuff\$3 or classif\$7)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 17:16
S156	16	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) near CAM same (IP or address or packet) near (lookup or match\$3 or process\$3) and (stuff \$3 or unstuff\$3 or classif \$7 or mask\$3) with (packet or operation)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 17:21
S157	2	@ay<="2000" and (((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) with CAM same (IP or address or packet) near (lookup or match\$3 or process\$3) same (stuff \$3 or unstuff\$3 or classif \$7 or mask\$3) with (packet or operation)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 18:18

S158	9	@ay<="2000" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) with CAM and (IP or address or packet) near (lookup or match\$3 or process\$3) same (stuff \$3 or unstuff\$3 or classif \$7 or mask\$3) with (packet or operation)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 18:49
S159	101	@ay<="2000" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) with CAM and (IP or address or packet or operation) near (lookup or match\$3 or process\$3 or stuff\$3 or unstuff\$3 or classif\$7 or mask\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 18:19
S160	0	@ay<="2000" and ((ternary adj content adj address\$4 adj memory) or TCAM or (ternary adj CAM)) with CAM and compar\$3 with (high\$2 near bit) with (register or operand)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 18:50
S161	49	(netlogic or Nataraj) and (TCAM or (ternary adj (CAM or (content adj address\$4 adj memory))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 18:53
S162	2	@ay<="2000" and (netlogic or Nataraj) and (TCAM or (ternary adj (CAM or (content adj address\$4 adj memory)))) and (process \$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) with (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 18:57

S163	6	@ay<="2000" and (netlogic or Nataraj) and (TCAM or (tenary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 18:59
S164	113	@ay<="2000" and (Nataraj)".in" and (TCAM or (tenary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 18:59
S165	0	@ay<="2000" and (bindiganavale near natarag) and (TCAM or (tenary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 21:21
S166	0	(bindiganavale near natarag) and (TCAM or (tenary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:19
S167	0	(bindiganavale near nataraj) and (TCAM or (tenary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:00

S168	0	(bindiganavale near nataraj).in. and (TCAM or (tenary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:01
S169	0	(nataraj).in. and (TCAM or (tenary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:01
S170	0	(nataraj).in. and (TCAM or (tenary adj (CAM or (content adj address\$4 adj memory)))) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:19
S171	43	@ay<="2001" and (TCAM or (tenary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:02
S172	26	@ay<="2001" and (TCAM or (tenary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address) and ("32" or "84") adj bit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:15
S178	0	"6757779".pn. and (TCAM or (tenary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:18

S179	0	(09/999800 or 09/590642 or 09/406170) and (TCAM or (ternary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:18
S180	0	"6757779".pn. and (TCAM or (ternary adj (CAM or (content adj address\$4 adj memory))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:19
S181	1	"6757779".pn. and (TCAM or (ternary adj (CAM or (content adj address\$4 adj memory))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 14:48
S182	0	(bindiganavale near natarag) and (TCAM or (ternary adj (CAM or (content adj address\$4 adj memory)))) and (process\$3 or mask\$3 or match\$3 or compar\$3 or classif\$7 or load\$3) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:19
S183	15	(nataraj).in. and (TCAM or (ternary adj (CAM or (content adj address\$4 adj memory)))) and (IP adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:20
S184	15	(nataraj).in. and (TCAM or (ternary adj (CAM or (content adj address\$4 adj memory)))) and (IP adj address) and bit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:54
S185	5	"6000016" and (TCAM or ((CAM or (content adj address\$4 adj memory)))) and ((IP adj address) or bit or perform\$3 or match\$3 or compar\$3 or operation)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 19:55
S186	316	@ay<="2000" and (TCAM or CAM) same (stuff\$3 or unstuff\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 21:22

S187	19	@ay<="2000" and (TCAM or CAM) with (stuff\$3 or unstuff\$3) with (packet or data or address or operation)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 21:23
S188	0	@ay<="2000" and (content adj address\$4 adj memory) same (stuff\$3 or unstuff\$3) with (packet or data or address or operation)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 17:15
S189	2	@ay<="2001" and (content adj address\$4 adj memory) same (stuff\$3 or unstuff\$3) with (packet or data or address or operation)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 21:25
S190	647	@ay<="2001" and (content adj address\$4 adj memory) and (stuff\$3 or unstuff\$3 or (match\$3 and replac\$3)) with (packet or data or address or operation)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 21:26
S191	32	@ay<="2001" and (content adj address\$4 adj memory) same (stuff\$3 or unstuff\$3 or (match\$3 and replac\$3)) with (packet or data or address or operation)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/03 21:26
S195	61	@ay<="2000" and ((content adj address\$4 adj memory) or CAM or TCAM) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 17:16
S196	16	@ay<="2000" and ((content adj address\$4 adj memory) or CAM or TCAM) same (within or in or resident or locat\$3 or contain\$3) with (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 17:17
S197	16	@ay<="2000" and ((content adj address\$4 adj memory) or CAM or TCAM) same (within or in or inside or resident or locat\$3 or contain\$3) with (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 17:39

S198	52	@ay<="2000" and ((content adj address\$4 adj memory) or CAM or TCAM) same (within or in or inside or resident or locat\$3 or contain\$3 or integrat\$3) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 17:59
S199	20	@ay<="2000" and ((content adj address\$4 adj memory) or CAM or TCAM) with (within or in or inside or resident or locat\$3 or contain\$3 or integrat\$3) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 17:59
S200	78	@ay<="2000" and ((content adj address\$4 adj memory) or CAM or TCAM or (memory adj register)) with (within or in or inside or resident or locat\$3 or contain\$3 or integrat\$3) same (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 19:43
S201	177	@ay<="2000" and ((content adj address\$4 adj memory) or CAM or TCAM or (memory adj register)) with (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 20:11
S202	22	@ay<="2000" and ((content adj address\$4 adj memory) or CAM or TCAM) with (ALU or (arithmetic adj logic adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 20:20
S203	63	@ay<="2000" and ((content adj address\$4 adj memory) or CAM or TCAM) same (ALU\$3 or (arithmetic adj logic\$2 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 20:51

S204	55	@ay< = "2000" and ((content adj address\$4 adj memory) or CAM or TCAM or ((embedd\$3 or integrat\$3) near3 memory)) with (ALU\$3 or (arithmetic adj logic\$2 adj unit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/04 21:25
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